

#### **1** General description

The 74AUP3G34 is a triple buffer.

Schmitt trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

This device ensures a very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing a damaging backflow current through the device when it is powered down.

#### 2 Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.2 V to 1.95 V)
  - JESD8-5 (1.8 V to 2.7 V)
  - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
  - HBM JESD22-A114F Class 3A exceeds 5000 V
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; I<sub>CC</sub> = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78B Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

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## **3** Ordering information

Table 1. Ordering	information			
Type number	Package			
	Temperature range	Name	Description	Version
74AUP3G34DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1
74AUP3G34GT	-40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 x 1.95 x 0.5 mm	SOT833-1
74AUP3G34GF	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 x 1 x 0.5 mm	SOT1089
74AUP3G34GM	-40 °C to +125 °C	XQFN8	plastic, extremely thin quad flat package; no leads; 8 terminals; body 1.6 x 1.6 x 0.5 mm	SOT902-2
74AUP3G34GN	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.2 x 1.0 x 0.35 mm	SOT1116
74AUP3G34GS	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 x 1.0 x 0.35 mm	SOT1203
74AUP3G34GX	-40 °C to +125 °C	X2SON8	plastic thermal enhanced extremely thin small outline package; no leads; 8 terminals; body 1.35 x 0.8 x 0.35 mm	SOT1233

## 4 Marking

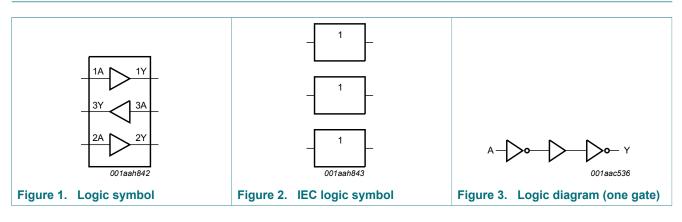
#### Table 2. Marking codes

Type number	Marking code <sup>[1]</sup>
74AUP3G34DC	a34
74AUP3G34GT	a34
74AUP3G34GF	aA
74AUP3G34GM	a34
74AUP3G34GN	aA
74AUP3G34GS	aA
74AUP3G34GX	aA

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

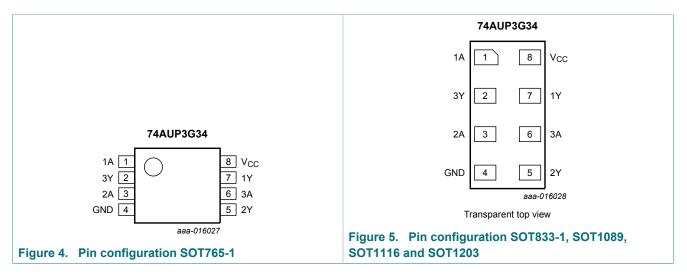
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### 5 Functional diagram

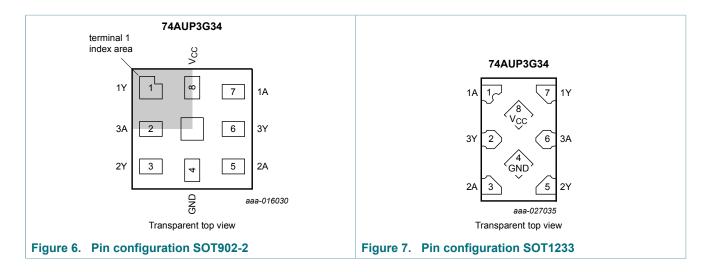


### 6 Pinning information

#### 6.1 Pinning



Low-power triple buffer



#### 6.2 Pin description

Table 3. Pin description

Symbol	Pin	Description	
	SOT765-1, SOT833-1, SOT1089, SOT1116, SOT1203 and SOT1233	SOT902-2	
1A, 2A, 3A	1, 3, 6	7, 5, 2	data input
1Y, 2Y, 3Y	7, 5, 2	1, 3, 6	data output
GND	4	4	ground (0 V)
V <sub>CC</sub>	8	8	supply voltage

### 7 Functional description

#### Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

Input	Output
nA	nY
L	L
Н	Н

### 8 Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Мах	Unit
V <sub>CC</sub>	supply voltage		-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>1</sub> < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode <sup>[1]</sup>	-0.5	+4.6	V
I <sub>O</sub>	output current	$V_{O} = 0 V \text{ to } V_{CC}$	-	±20	mA
I <sub>CC</sub>	supply current		-	+50	mA
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C <sup>[2]</sup>	-	250	mW

The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.
 For VSSOP8 packages: above 110 °C the value of P<sub>tot</sub> derates linearly with 8.0 mW/K.

[2] For VSSOP8 packages: above 110 °C the value of P<sub>tot</sub> derates linearly with 8.0 mW/K. For XSON8 and XQFN8 packages: above 118 °C the value of P<sub>tot</sub> derates linearly with 7.8 mW/K. For X2SON8 package: above 118 °C the value of P<sub>tot</sub> derates linearly with 7.7 mW/K.

## 9 Recommended operating conditions

#### Table 6. Operating conditions

Symbol	Parameter	Conditions	Min	Мах	Unit
V <sub>CC</sub>	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 0.8 V to 3.6 V	-	200	ns/V

## **10 Static characteristics**

#### Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = 25	°C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	$0.35V_{CC}$	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{\rm O}$ = -20 µA; $V_{\rm CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.32	-	-	V
		$I_{\rm O}$ = -2.3 mA; $V_{\rm CC}$ = 2.3 V	2.05	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	V
		$I_{\rm O}$ = -2.7 mA; $V_{\rm CC}$ = 3.0 V	2.72	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{O}$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V
I	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.1	μA
I <sub>OFF</sub>	power-off leakage current	$V_1$ or $V_0$ = 0 V to 3.6 V; $V_{CC}$ = 0 V	-	-	±0.2	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μA
I <sub>CC</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
Δl <sub>cc</sub>	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	40	μA

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#### Low-power triple buffer

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
CI	input capacitance	$V_{CC}$ = 0 V to 3.6 V; V <sub>I</sub> = GND or V <sub>CC</sub>	-	1.0	-	pF
Co	output capacitance	$V_{O}$ = GND; $V_{CC}$ = 0 V	-	1.8	-	pF
T <sub>amb</sub> = -4	0 °C to +85 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
VIL	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{\rm O}$ = -20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		$I_{\rm O}$ = -2.3 mA; $V_{\rm CC}$ = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		$I_{O}$ = -2.7 mA; $V_{CC}$ = 3.0 V	2.67	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.55	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{\rm O}$ = 20 µA; $V_{\rm CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.5	μA
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V	-	-	±0.5	μA
Δl <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.6	μA
I <sub>CC</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.9	μA
ΔI <sub>CC</sub>	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	50	μA

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#### Low-power triple buffer

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit			
T <sub>amb</sub> = -4	0 °C to +125 °C								
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.75V <sub>CC</sub>	-	-	V			
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.70V <sub>CC</sub>	-	-	V			
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V			
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V			
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.25V <sub>CC</sub>	V			
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.30V <sub>CC</sub>	V			
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V			
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V			
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$							
		$I_{\rm O}$ = -20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V			
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6V <sub>CC</sub>	-	-	V			
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V			
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V			
		$I_{\rm O}$ = -2.3 mA; $V_{\rm CC}$ = 2.3 V	1.77	-	-	V			
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V			
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V			
		$I_{O}$ = -4.0 mA; $V_{CC}$ = 3.0 V	2.30	-	-	V			
V <sub>OL</sub>	LOW-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>							
		$I_{O}$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.11	V			
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33V <sub>CC</sub>	V			
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V			
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V			
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V			
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V			
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V			
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V			
I <sub>I</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.75	μA			
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V	-	-	±0.75	μA			
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.75	μA			
I <sub>CC</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μA			
ΔI <sub>CC</sub>	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	75	μA			

## **11** Dynamic characteristics

#### Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit, see Figure 9.

Symbol	Parameter	neter Conditions		25 °C		-40	°C to +1	25 °C	Uni
			Min	Typ <sup>[1]</sup>	Мах	Min	Max (85 °C)	Max (125 °C)	
C <sub>L</sub> = 5 pF		1		1		<u> </u>		1	
pd	propagation delay	nA to nY; see Figure 8							_
		• V <sub>CC</sub> = 0.8 V	-	14.9	-	-	-	-	ns
		• V <sub>CC</sub> = 1.1 V to 1.3 V	2.6	4.7	9.2	2.0	10.0	11.0	ns
		• V <sub>CC</sub> = 1.4 V to 1.6 V	2.1	3.4	5.7	1.6	6.5	7.2	ns
		• V <sub>CC</sub> = 1.65 V to 1.95 V	1.8	2.9	4.5	1.4	5.2	5.8	ns
		<ul> <li>V<sub>CC</sub> = 2.3 V to 2.7 V</li> </ul>	1.5	2.3	3.5	1.2	4.2	4.6	ns
		• V <sub>CC</sub> = 3.0 V to 3.6 V	1.4	2.1	3.2	1.0	3.8	4.2	ns
C <sub>L</sub> = 10 p	F						1	1	
pd	propagation delay	nA to nY; see <u>Figure 8</u> <sup>[2]</sup>							
		• V <sub>CC</sub> = 0.8 V	-	18.4	-	-	-	-	ns
		• V <sub>CC</sub> = 1.1 V to 1.3 V	3.2	5.6	10.9	2.3	11.8	13.1	ns
		• V <sub>CC</sub> = 1.4 V to 1.6 V	2.6	4.1	6.7	1.9	7.7	8.5	ns
		• V <sub>CC</sub> = 1.65 V to 1.95 V	2.3	3.4	5.3	1.7	6.2	6.9	ns
		• V <sub>CC</sub> = 2.3 V to 2.7 V	2.0	2.9	4.2	1.5	5.0	5.5	ns
		• V <sub>CC</sub> = 3.0 V to 3.6 V	1.7	2.6	3.8	1.4	4.6	5.1	ns
C <sub>L</sub> = 15 p	F						1	1	
pd	propagation delay	nA to nY; see <u>Figure 8</u> <sup>[2]</sup>							_
		• V <sub>CC</sub> = 0.8 V	-	21.9	-	-	-	-	ns
		• V <sub>CC</sub> = 1.1 V to 1.3 V	3.6	6.4	12.6	2.6	13.8	15.2	ns
		• V <sub>CC</sub> = 1.4 V to 1.6 V	3.0	4.6	7.6	2.2	8.9	9.8	ns
		• V <sub>CC</sub> = 1.65 V to 1.95 V	2.6	3.9	6.0	2.0	7.2	7.9	ns
		• V <sub>CC</sub> = 2.3 V to 2.7 V	2.3	3.3	4.8	1.8	5.7	6.3	ns
		• V <sub>CC</sub> = 3.0 V to 3.6 V	2.1	3.1	4.2	1.6	5.0	5.5	ns
C <sub>L</sub> = 30 p	F						1	1	_
pd	propagation delay	nA to nY; see Figure 8 [2]							
		• V <sub>CC</sub> = 0.8 V	-	32.1	-	-	-	-	ns
		• V <sub>CC</sub> = 1.1 V to 1.3 V	4.8	8.7	16.3	3.6	18.9	20.8	ns
		• V <sub>CC</sub> = 1.4 V to 1.6 V	4.0	6.2	10.3	3.4	12.2	13.4	ns
		• V <sub>CC</sub> = 1.65 V to 1.95 V	3.6	5.2	8.1	3.2	9.8	10.8	ns
		• V <sub>CC</sub> = 2.3 V to 2.7 V	3.0	4.4	6.4	2.7	7.7	8.5	ns
		• V <sub>CC</sub> = 3.0 V to 3.6 V	2.9	4.2	5.6	2.5	6.5	7.2	ns

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Symbol	mbol Parameter Conditions		25 °C		-40 °C to +125 °C			Unit	
			Min	Typ <sup>[1]</sup>	Max	Min	Max (85 °C)	Max (125 °C)	
C <sub>L</sub> = 5 pF	, 10 pF, 15 pF and 3	0 pF					,		
C <sub>PD</sub>	power dissipation	$f_i = 1 \text{ MHz}; V_I = \text{GND to } V_{\text{CC}}$ <sup>[3]</sup>	[4]						
	capacitance	• V <sub>CC</sub> = 0.8 V	-	2.5	-	-	-	-	pF
		• V <sub>CC</sub> = 1.1 V to 1.3 V	-	2.6	-	-	-	-	pF
		• V <sub>CC</sub> = 1.4 V to 1.6 V	-	2.7	-	-	-	-	pF
		• V <sub>CC</sub> = 1.65 V to 1.95 V	-	2.9	-	-	-	-	pF
		• V <sub>CC</sub> = 2.3 V to 2.7 V	-	3.4	-	-	-	-	pF
		• V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.0	-	-	-	-	pF

All typical values are measured at nominal  $\ensuremath{\mathsf{V}_{\text{CC}}}$ 

[1] [2] [3] [4]

 $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ . All specified values are the average typical values over all stated loads.

 $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu$ W).  $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$  where:

f<sub>i</sub> = input frequency in MHz;

fo = output frequency in MHz;

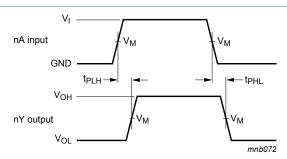
C<sub>L</sub> = load capacitance in pF;

 $V_{CC}$  = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$  = sum of the outputs.

#### 11.1 Waveforms and test circuit



Measurement points are given in Table 9.

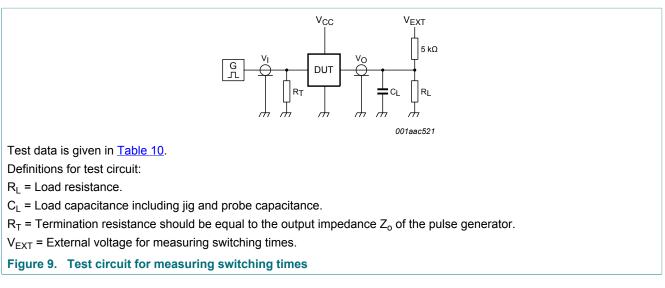
Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

#### Figure 8. The data input (nA) to output (nY) propagation delays

#### Table 9. Measurement points

Supply voltage	Output	Input		
V <sub>cc</sub>	V <sub>M</sub>	V <sub>M</sub>	VI	$t_r = t_f$
0.8 V to 3.6 V	0.5V <sub>CC</sub>	0.5V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns

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#### Table 10. Test data

Supply voltage	Load		V <sub>EXT</sub>		
V <sub>cc</sub>	CL	R <sub>L</sub> <sup>[1]</sup>	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF, and 30 pF	5 k $\Omega$ or 1 M $\Omega$	open	GND	2V <sub>CC</sub>

[1] For measuring enable and disable times  $R_L = 5 k\Omega_{..}$ , for measuring propagation delays, setup and hold times and pulse width  $R_L = 1 M\Omega_{..}$ 

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## 12 Package outline

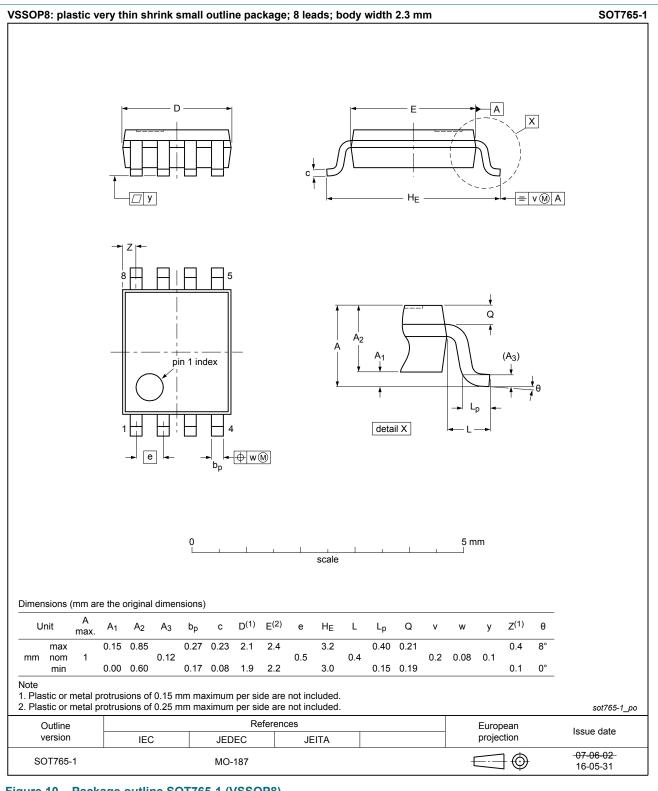
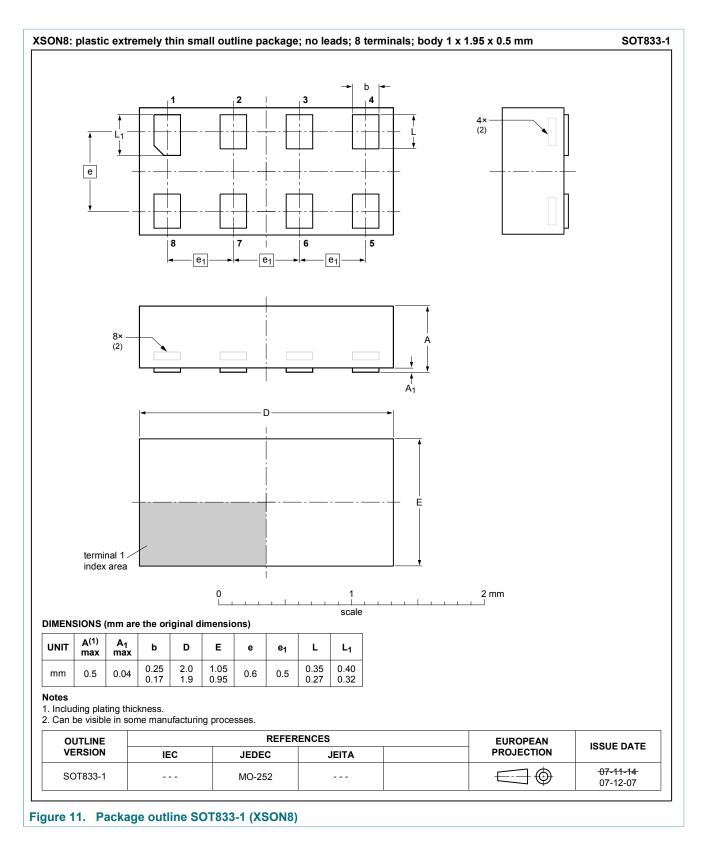


Figure 10. Package outline SOT765-1 (VSSOP8)

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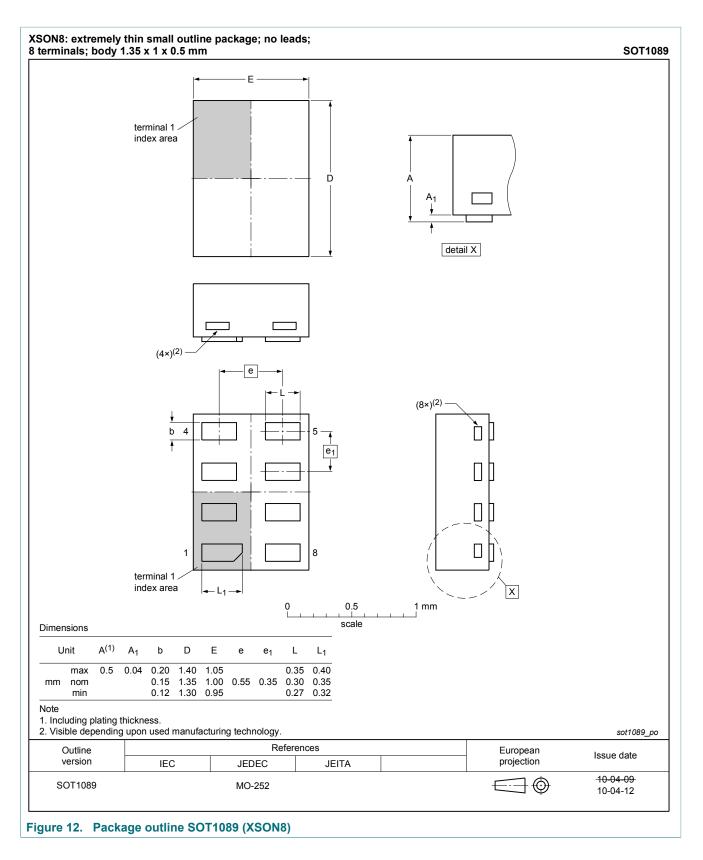
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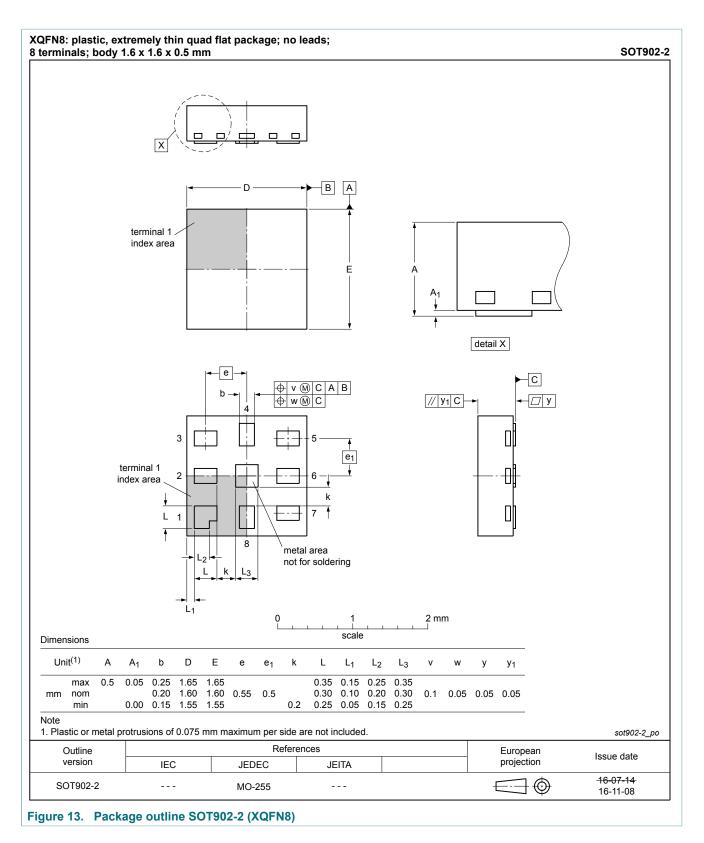


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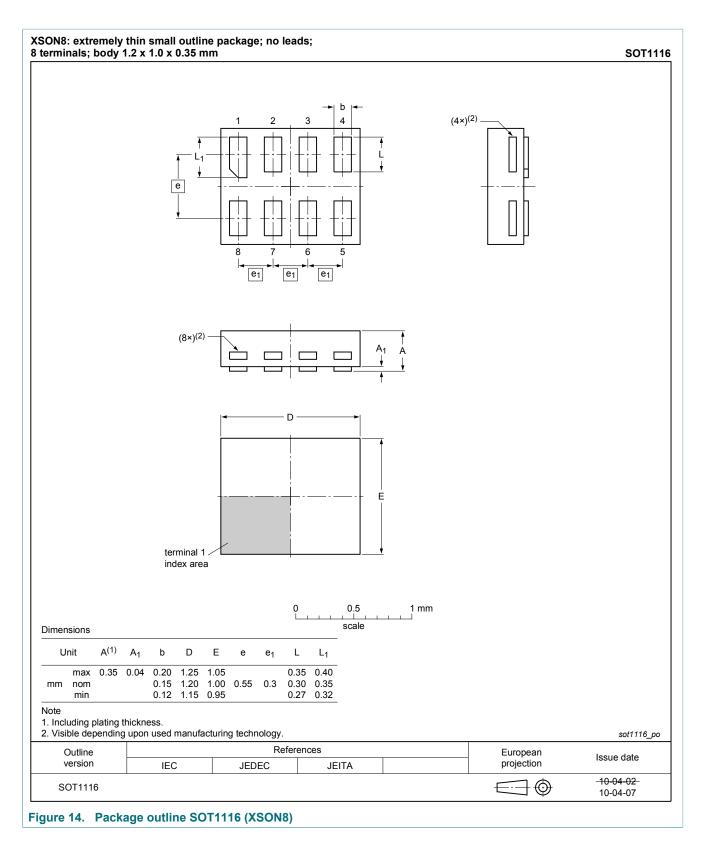
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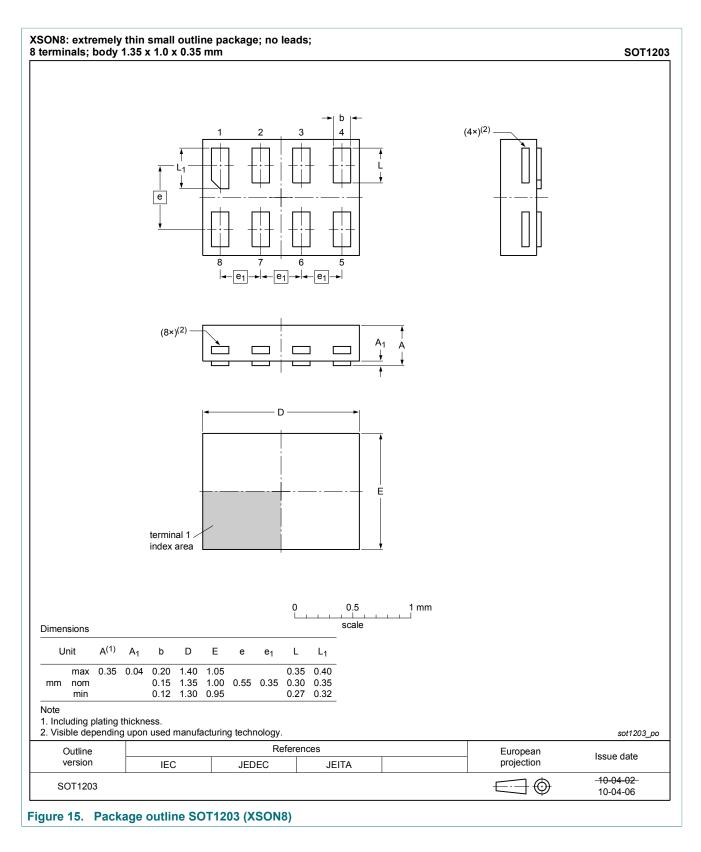
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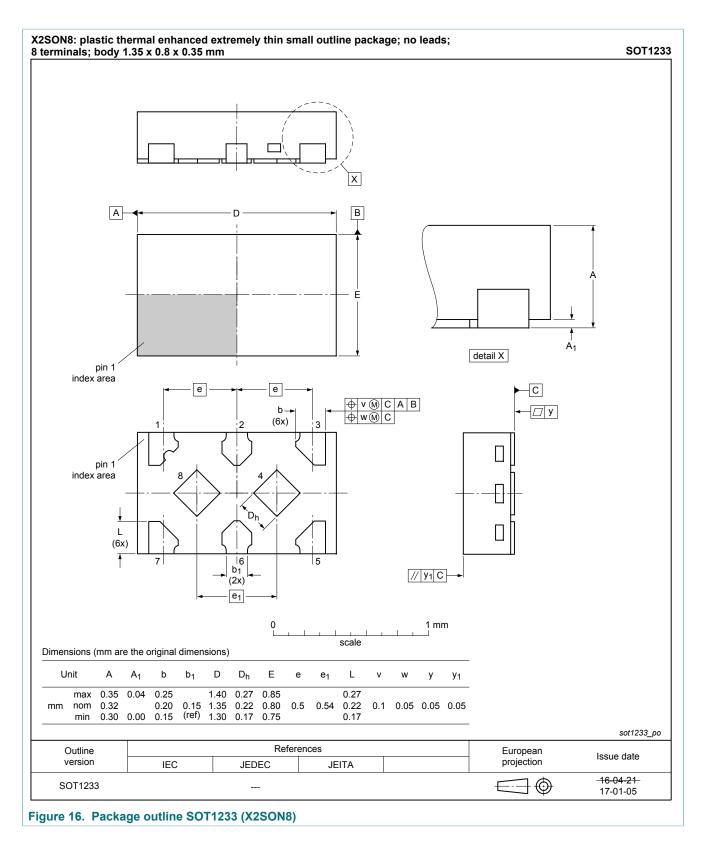
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#### Low-power triple buffer



## **13 Abbreviations**

Table 11. Abbreviations			
Acronym	Description		
CDM	Charged Device Model		
DUT	Device Under Test		
ESD	ElectroStatic Discharge		
НВМ	Human Body Model		
MM	Machine Model		

## 14 Revision history

#### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AUP3G34 v.3	20170703	Product data sheet	-	74AUP3G34 v.2	
Modifications:	Nexperia. • Legal texts hav • Type number 7	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Type number 74AUP3G34GX (SOT1233 / X2SON8) added.</li> <li>Type number 74AUP3G34GD removed.</li> </ul>			
74AUP3G34 v.2	20161011	Product data sheet	-	74AUP3G34 v.1	
Modifications:	<ul> <li>Type numbers</li> </ul>	Type numbers 74AUP3G34DP removed.			
74AUP3G34 v.1	20141218	Product data sheet	-	-	

## 15 Legal information

#### 15.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

The term 'short data sheet' is explained in section "Definitions".

[2] [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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